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First Named

Applicant: Kyung Joon Han et al.

Docket No.: 11188.00

Title: METHOD AND APPARATUS FOR MULTIPLE BYTE OR PAGE MODE
PROGRAMMING AND READING AND FOR ERASING OF A FLASH
MEMORY ARRAY

Serial No.: 10/039,518


Filed: 08 November 2001

Examiner:

Group Art Unit: 2818

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I hereby certify that this document is being deposited with the United States Postal Service "Express Mail Post Office to Address" service under 37 CFR 1.10 in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231 on this 25th day of March, 2003.


Name: Kristi Murray

Commissioner for Patents
Washington, D.C. 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR § 1.97(b)

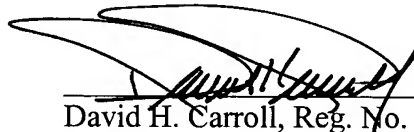
This Information Disclosure Statement is being filed concurrently with the above-referenced patent application. In accordance with 37 CFR § 1.97(b) to the present knowledge of the undersigned, the accompanying Information Disclosure Citation and the information contained herein are being filed within any one of the following time periods: within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR § 1.491 in an international application; before the mailing of a first Office action on the merits; or before the mailing of a first Office action after the filing of a request for continued examination under 37 CFR § 1.114. Pursuant to 37 CFR § 1.97(b), no fee is required. However,

The filing of this information disclosure statement, including citation of the listed documents, shall not be construed as an admission that the information furnished is, or is considered to be, material to patentability as defined in, for example, section 1.56(b), or prior art with respect to the present invention, or as a representation that that no other material information exists or that search has been made (other than specifically described above). The filing of this information disclosure statement shall not be construed as an admission against interest in any manner.

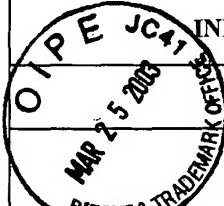
The cited pages to reference "M" on the Form PTO-1449 all relate to various types of memory cell disturbance, including program, erase and read disturb, as identified in the Table of contents and in the Index of the reference. Applicants have confirmed that a copy of the entire reference is available from the United States Patent Office Technical Library, for the examiner's use as desired.

Respectfully submitted,

Date: 25 March 2003



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	INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)		Docket Number (Optional) 11188.00	Application Number 10/039,518
			Applicant(s) Kyung J on Han et al.	
			Filing Date 08 November 2001	Group Art Unit 2818

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

M	Brown, William D. and Brewer, Joe E., eds., Nonvolatile Semiconductor Memory Technology: A Comprehensive Guide to Understanding and Using NVSM Devices, IEEE Press, New York, 1998, pp. 69, 105, 107-108, 203, 213-217, 226-227, 235, 239-241, 244, 247, 279-281.
EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609, Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	